

FIG. 1
(PRIOR ART)

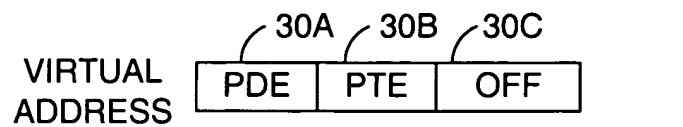


FIG. 2A
(PRIOR ART)

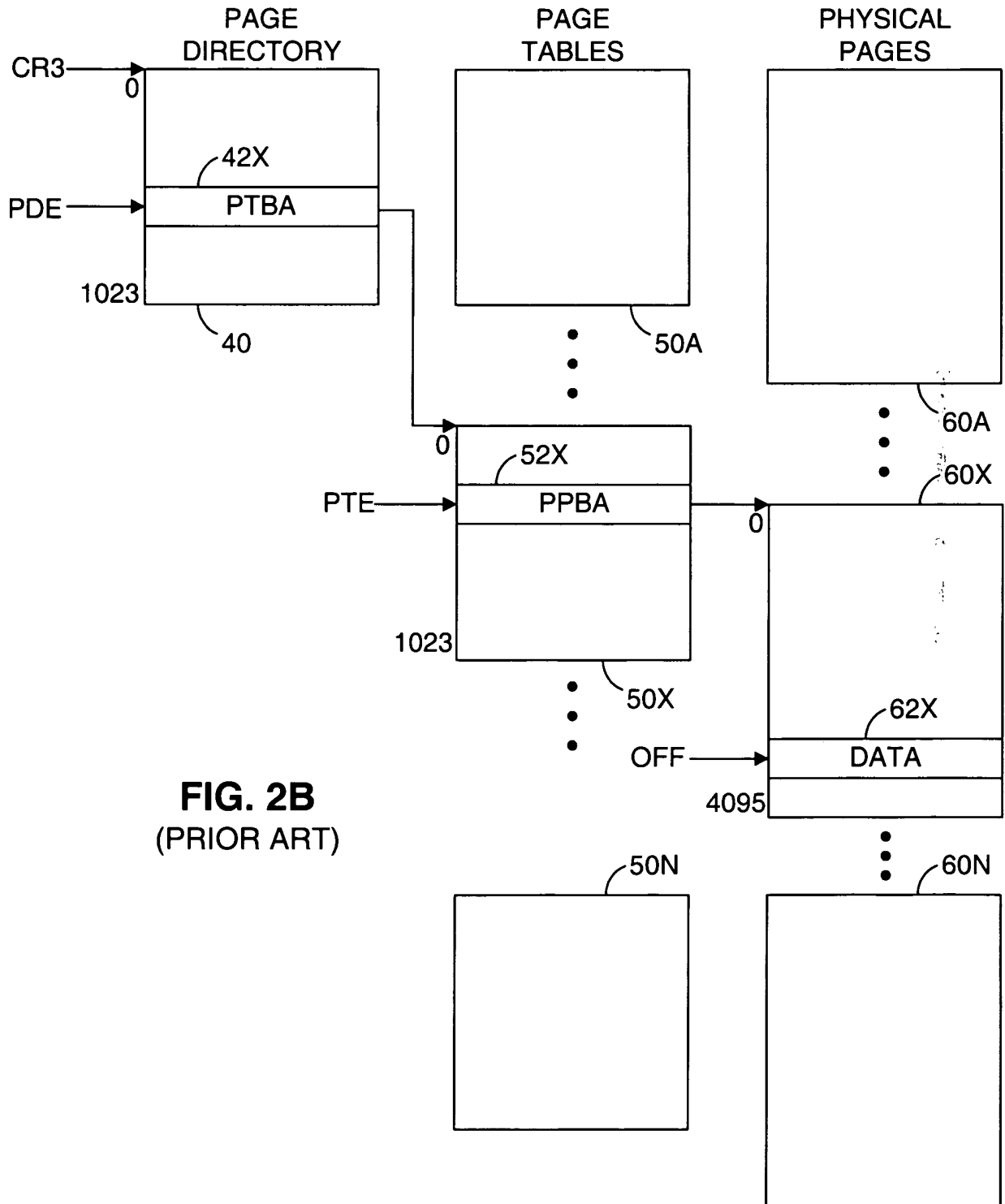


FIG. 2B
(PRIOR ART)

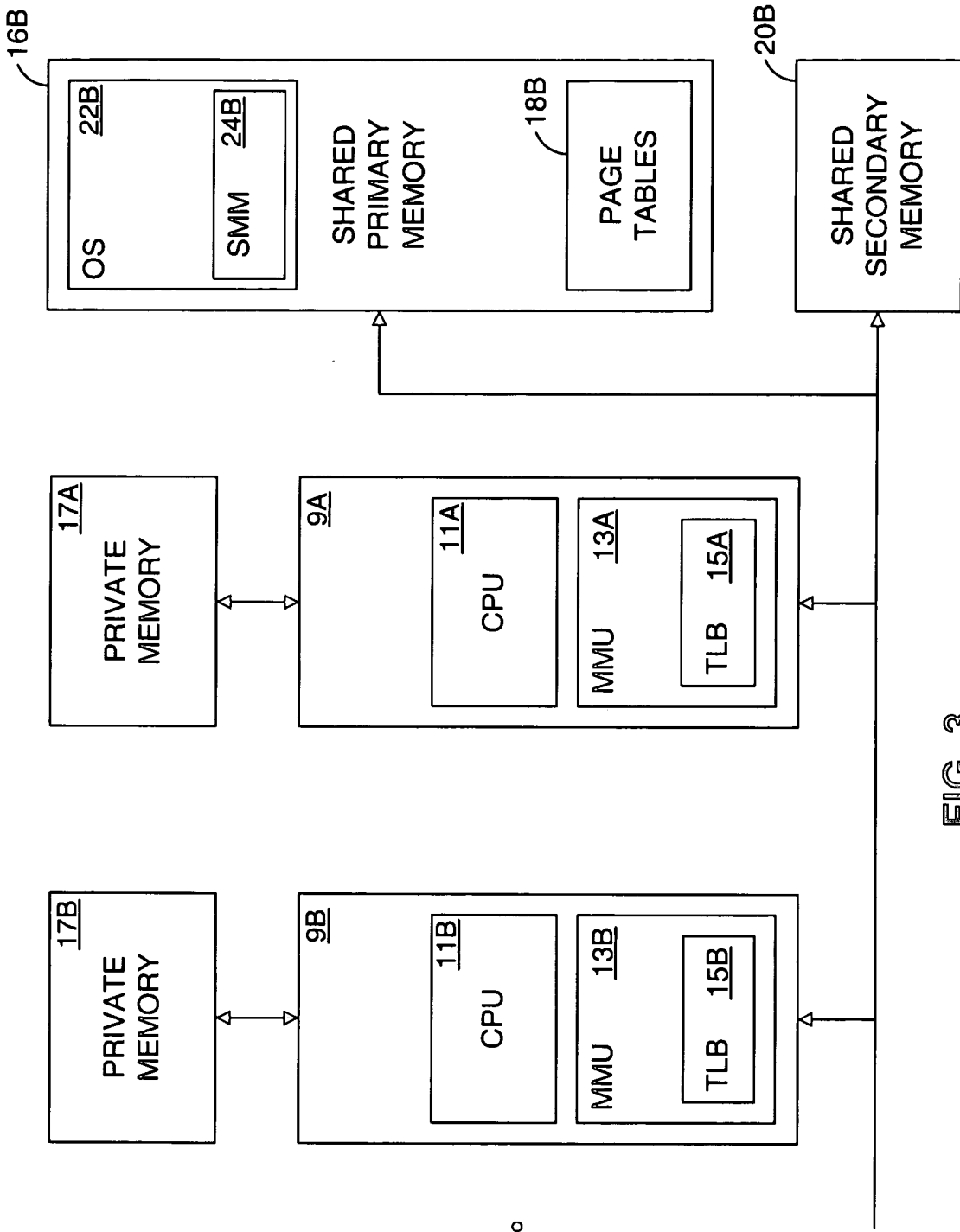


FIG. 3
(PRIOR ART)

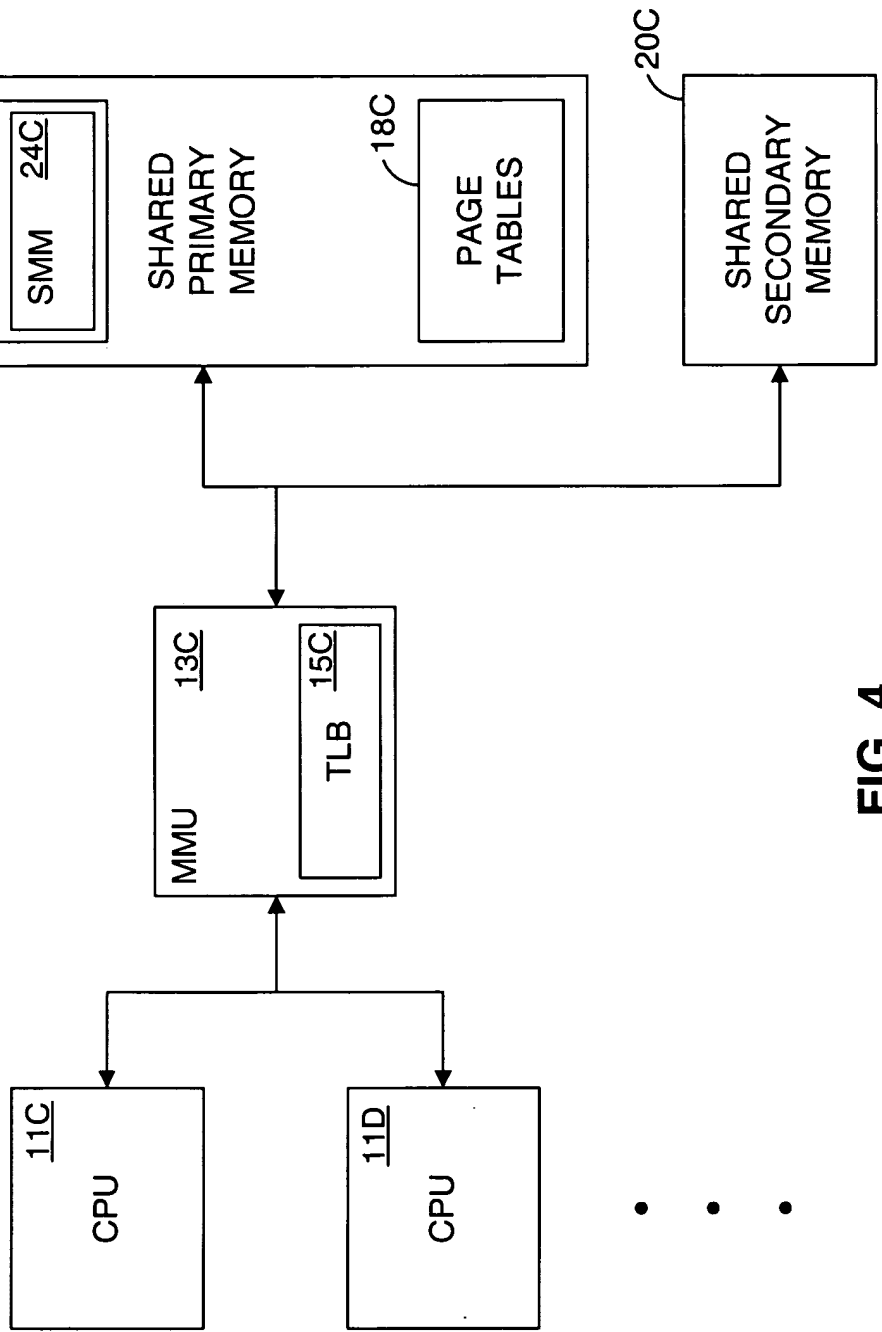
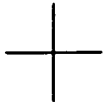
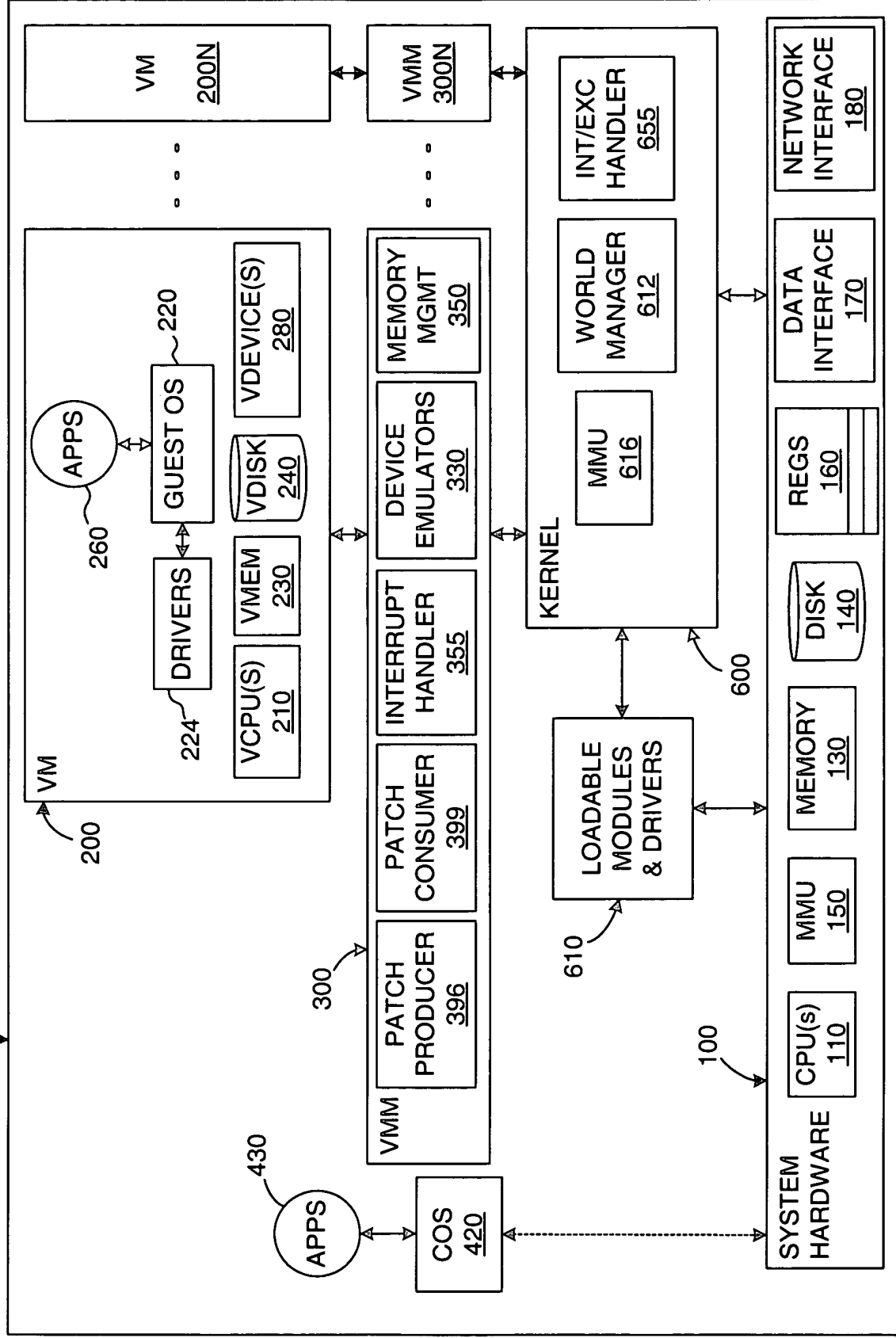


FIG. 4
(PRIOR ART)



FIG. 5



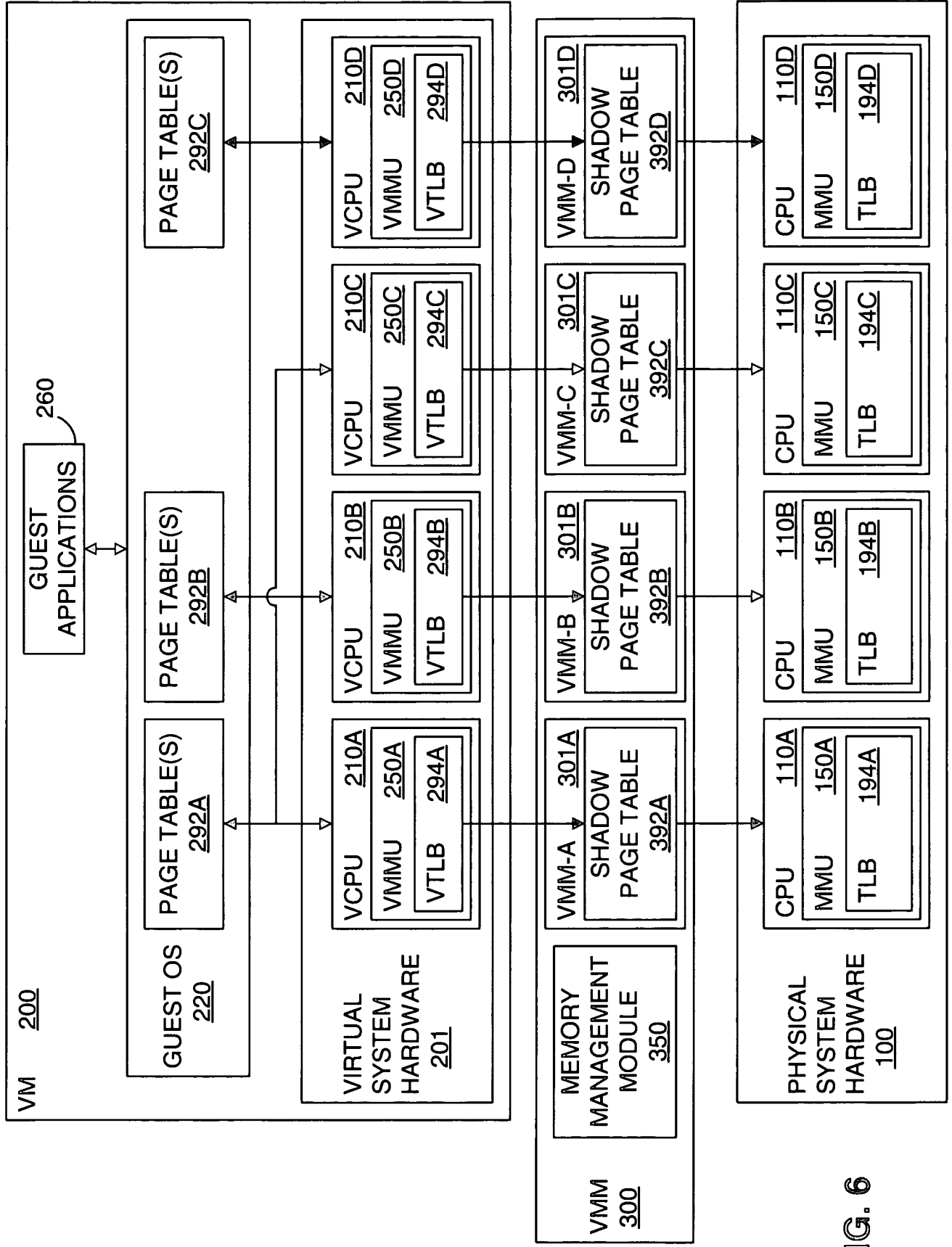


FIG. 6

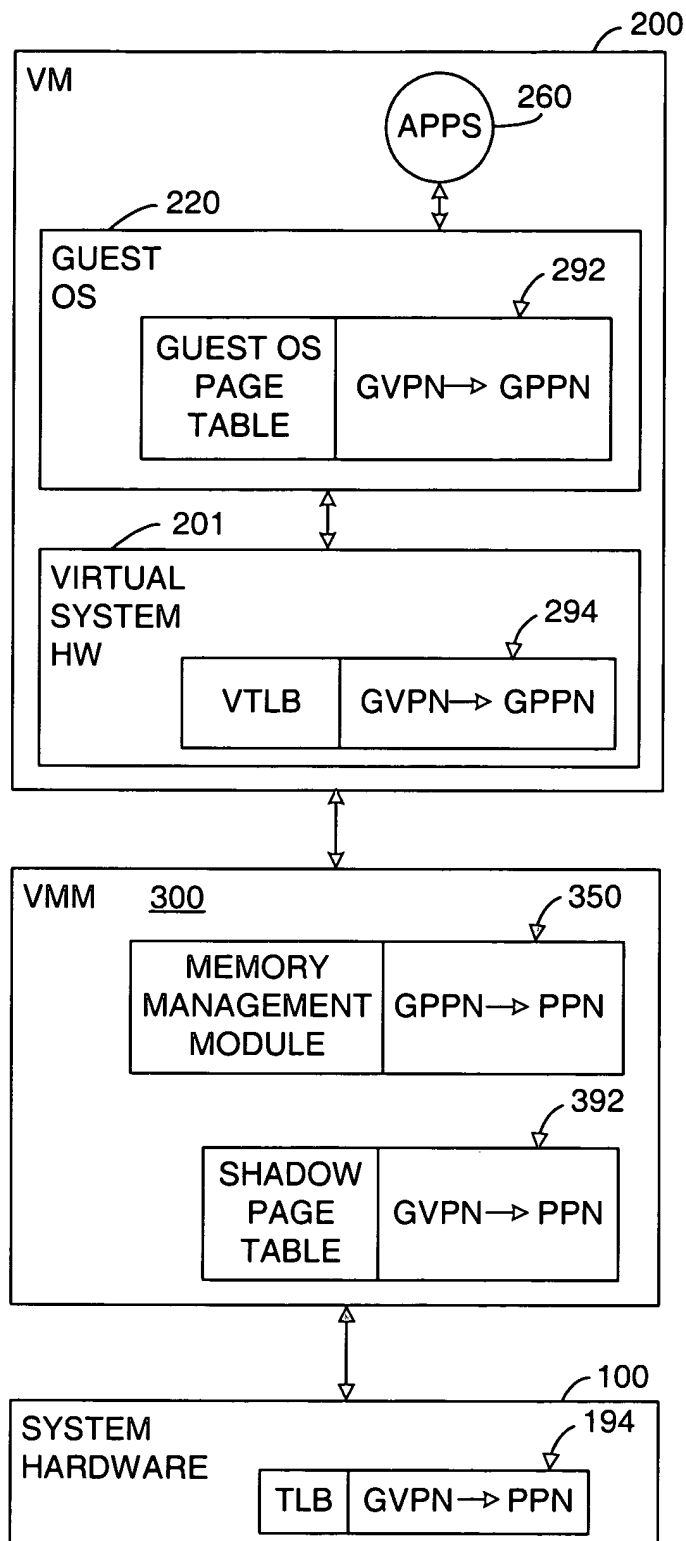


FIG. 7

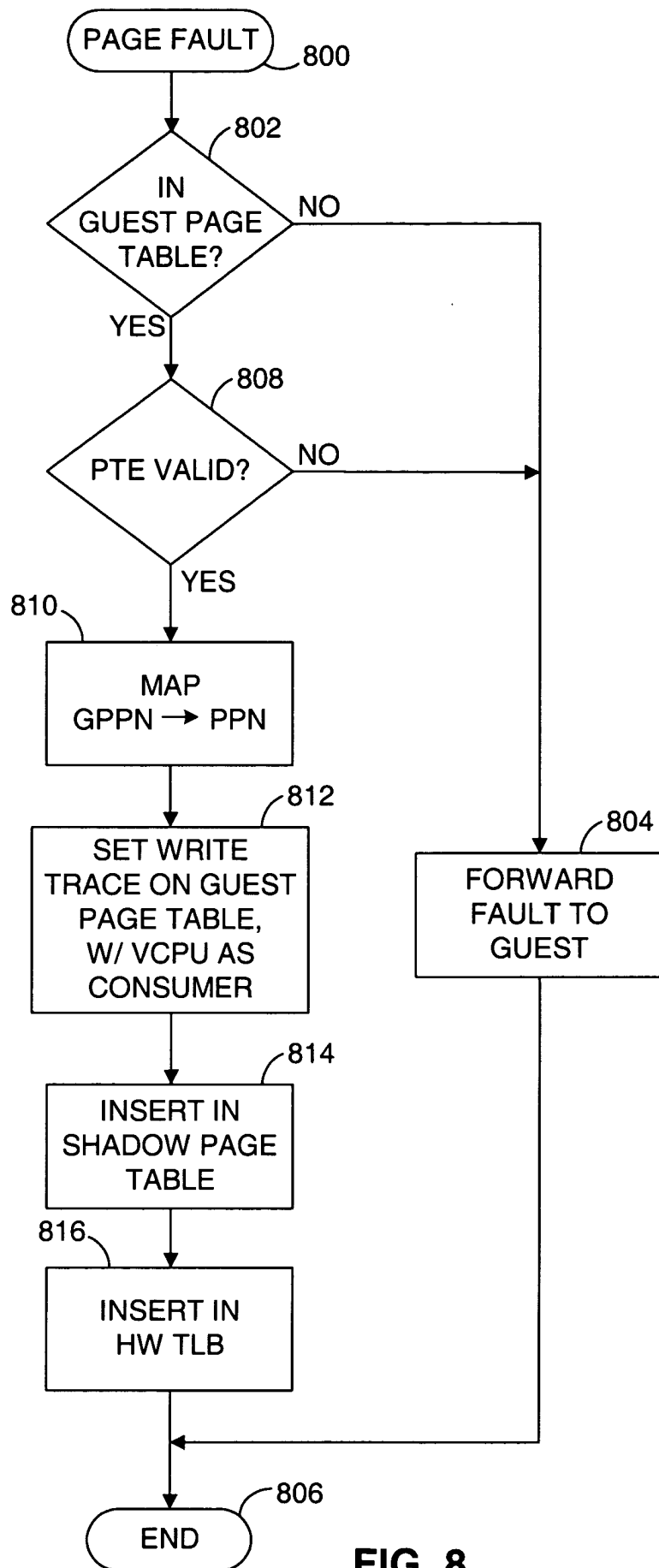


FIG. 8

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PRODUCER TO CONSUMER PATCH CHANNEL (P2C PC)					
PATCH #	PTBA	OFFSET	DER. VAL.	USEFUL?	IN USE?
PATCH 0	PTBA 0	OFFSET 0	DER. VAL. 0	USEFUL 0	IN USE 0
PATCH 1	PTBA 1	OFFSET 1	DER. VAL. 1	USEFUL 1	IN USE 1
PATCH 2	PTBA 2	OFFSET 2	DER. VAL. 2	USEFUL 2	IN USE 2
•	•	•	•	•	•
•	•	•	•	•	•
•	•	•	•	•	•
PATCH N	PTBA N	OFFSET N	DER. VAL. N	USEFUL N	IN USE N

394A 394B 394C 394D 394E 394F

FIG. 9

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		CONSUMER			
		399A	399B	399C	399D
PRODUCER	396A	A2A PC <u>398A</u>	A2B PC <u>398B</u>	A2C PC <u>398C</u>	A2D PC <u>398D</u>
	396B	B2A PC <u>398E</u>	B2B PC <u>398F</u>	B2C PC <u>398G</u>	B2D PC <u>398H</u>
	396C	C2A PC <u>398I</u>	C2B PC <u>398J</u>	C2C PC <u>398K</u>	C2D PC <u>398L</u>
	396D	D2A PC <u>398M</u>	D2B PC <u>398N</u>	D2C PC <u>398O</u>	D2D PC <u>398P</u>

FIG. 10

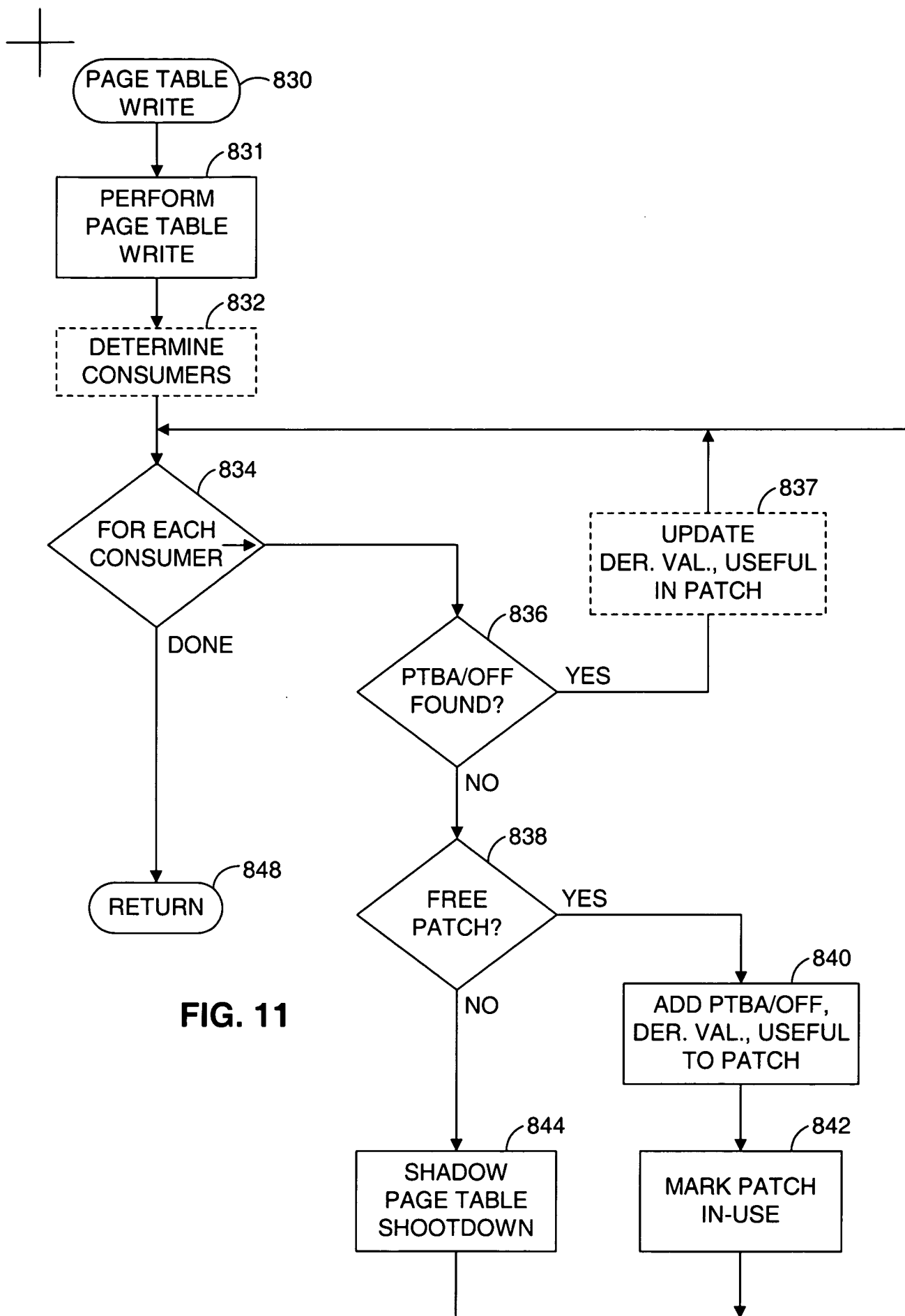


FIG. 11

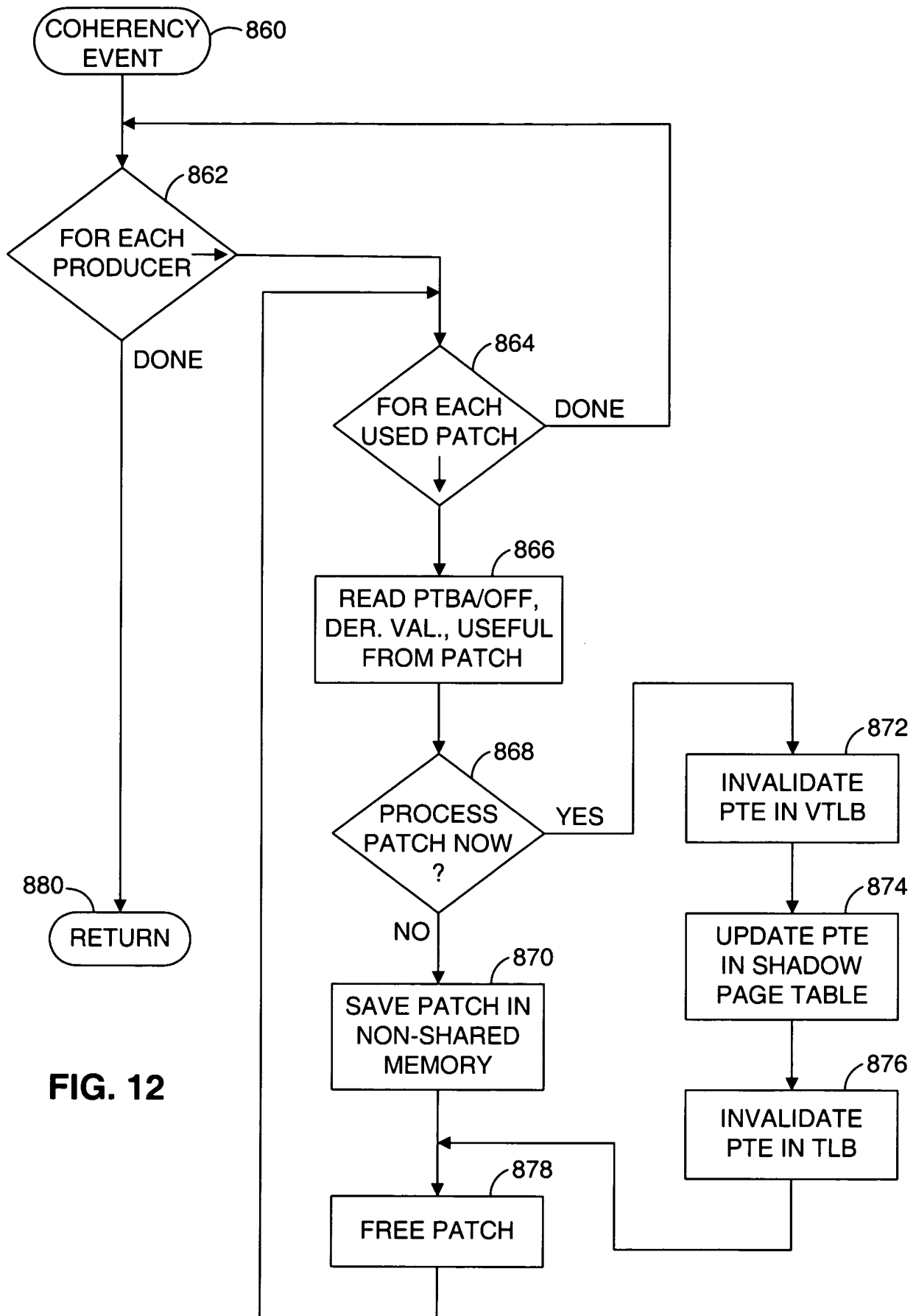
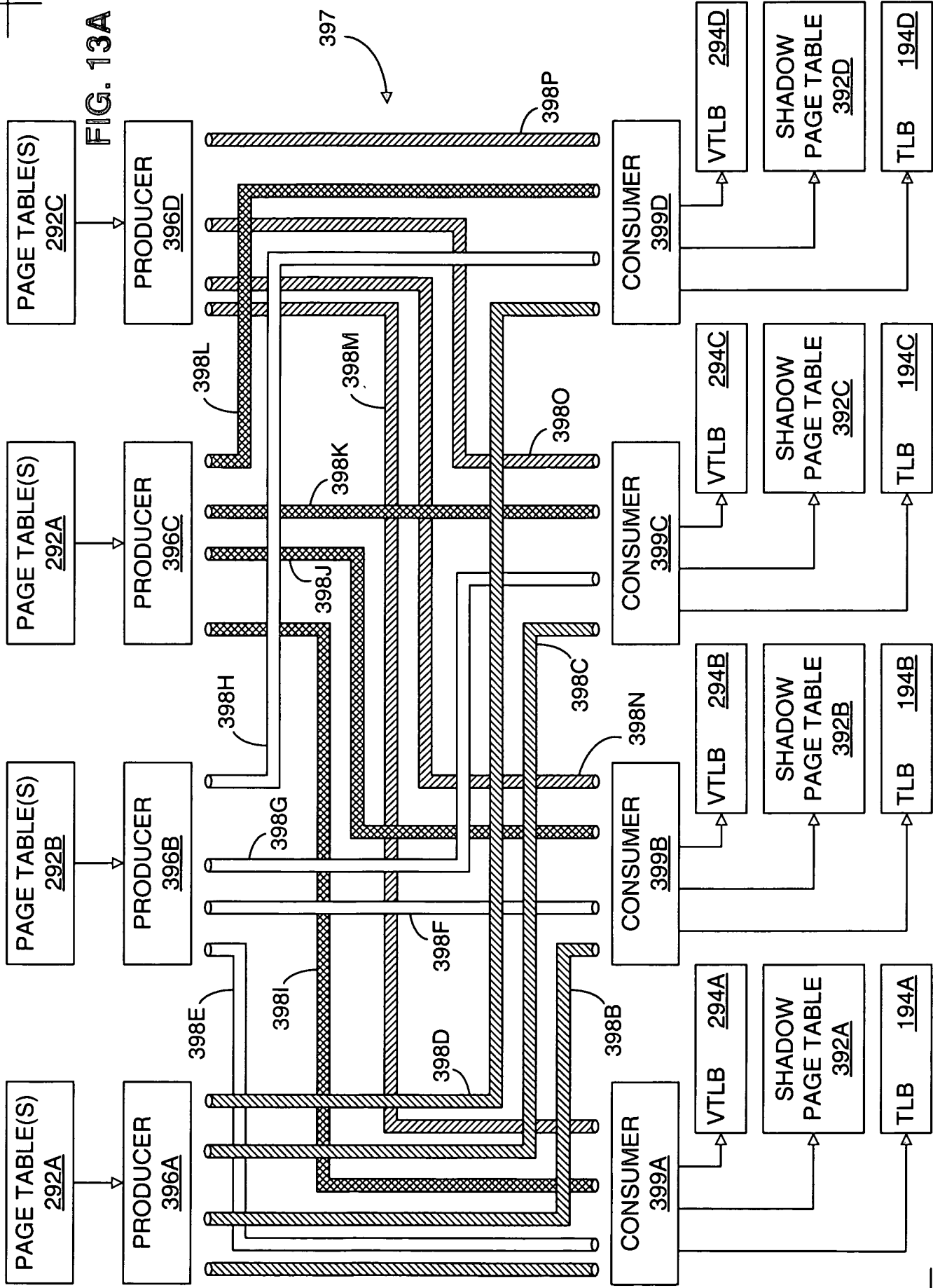


FIG. 12



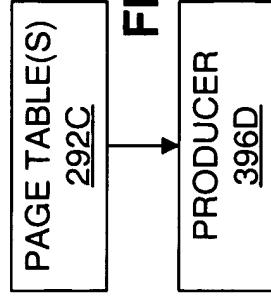
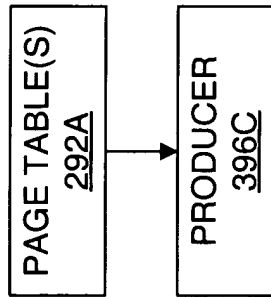
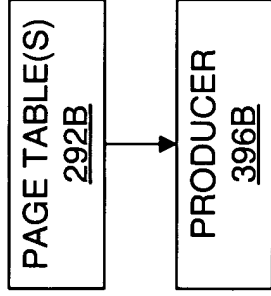
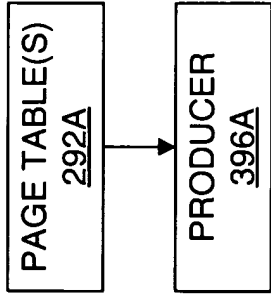
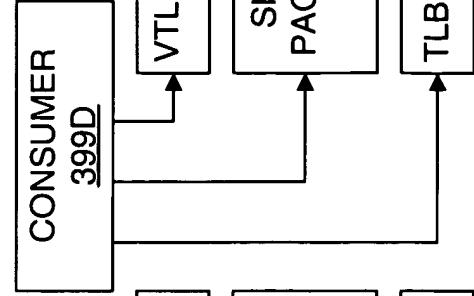
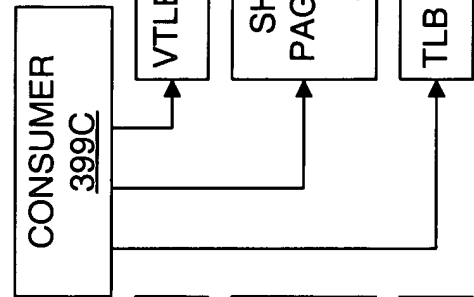
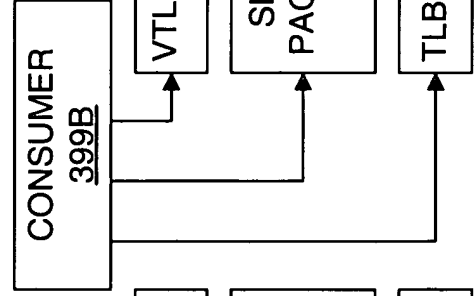
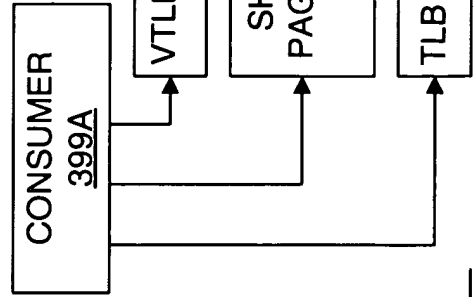
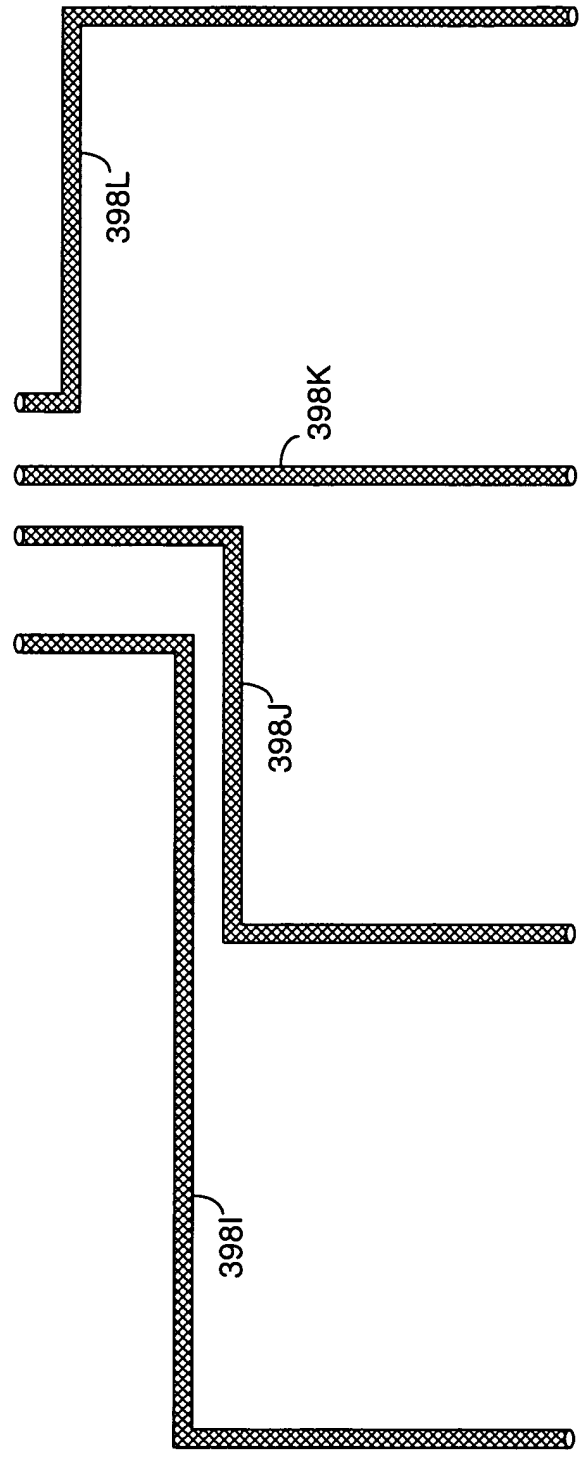


FIG. 13B



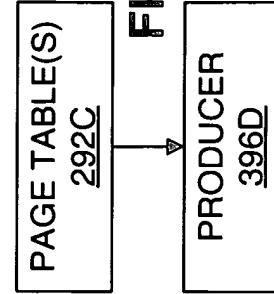
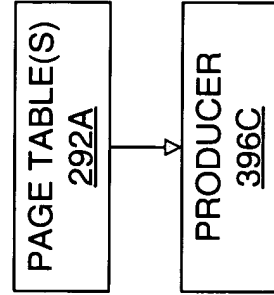
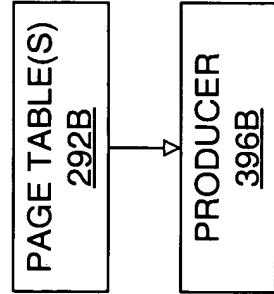
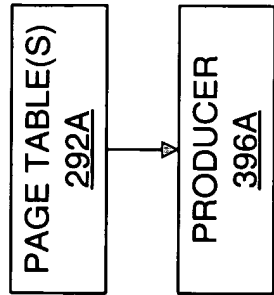


FIG. 13C

